



DEPARTMENT OF ELECTRONICS ENGINEERING
डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

क्रमांक: DoECE / Ph.D. Adm/ 1740 / 2025-26

दिनांक: 27/11/2025

Ph. D. Admission (Academic Year-2025-26)

List of Eligible/ Provisionally Eligible Candidates for the DoECE, DEC – 2025

Sr. No.	App. ID	User Name	Candidate's Name	Ph.D. Cat.	Eligible / PE / NE
1.	12533	SVNIT3PPO9	Dubekar Gayatri Sunil	ERS	PE
2.	12540	SVNIT3PAUC	Badgujar Sejal Nilkanth	ERS	PE
3.	12549	SVNIT3P7ZZ	Deshmukh Rohit Nitin	ERS	PE
4.	12604	SVNIT3PXON	Prashant Goyal	FIR	PE
5.	12654	SVNIT3PK6W	Virendra Abhesing Patel	FPS	PE
6.	12664	SVNIT3PLXG	Mitkar Avinash	ERS	PE
7.	12676	SVNIT3PMT7	Dhengale Ganesh Namdeo	ERS	PE
8.	12724	SVNIT3PSDV	Bhusa devi	ERS	PE
9.	12735	SVNIT3P2VQ	Chandratre Yogesh Vijay	ERS	PE
10.	12738	SVNIT3PU1E	Panchariya Narendra	FIR	PE
11.	12748	SVNIT3PBT2	Momin Khalil Ahmed	ERS	PE
12.	12811	SVNIT3P7OA	Parmar Nidhi Navinbhai	FIR	PE
13.	12816	SVNIT3PKZI	Gore Shukracharya	ERS	PE
14.	12818	SVNIT3PGKX	Mankar Prasanth	ERS	PE
15.	12835	SVNIT3P5ZH	Parmar Jayantkumar	FIR	PE
16.	12836	SVNIT3P8PN	Deshmukh Vaishnavi	FIR	PE

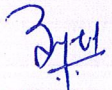
Note: E - Eligible, PE- Provisionally Eligible, NE- Not Eligible

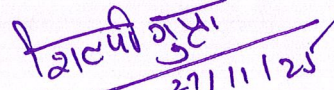
- The candidates must produce original documents with relevant **mark-sheets of all semesters** as well as **final degree certificate** of B.E./B. Tech. and M.E./M. Tech., B.Sc/M.Sc. at the time/day of the interview. Failing which they will not be allowed to participate in admission process.
- Candidates applied under OBC category must produce **recent(April-2025 onwards) NCL** in English language.
- Candidates who have applied in FIR category need to produce the **GATE/NET Score card**.
- All **PE** candidates applied under **ERS(Part Time)** category for Ph.D.Admission must enclose self-attested copies of the following documents/certificates. Same must be produced in original at the time of interview/physical documents verification.
 - Appointment letter and sponsorship certificate/no objection certificate issued by the current employer
 - Experience certificate duly signed by competent authority on the letter head of the Company/Organization/Institute.
 - The external candidates should produce fresh NOC/Sponsorship letter strictly as per format in Appendix III and VII.
 - Other documents like date of birth and identity proofs, marksheets of all semesters, degree certificates.

➤ For any queries contact office@eced.svnit.ac.in

Instruction For Candidates:

- The above Candidates are requested to Report at the Department of Electronics Engineering on **10/12/2025 at 9:00 AM** with **all Original Documents**. **Short-listed candidates must remain present for the interview/presentation at Room No. EC004N Conference room, Ground Floor.**
- The Presentation/ interview will **start at 11:30 AM**. Candidates are required to give a presentation as per the following guidelines.
 - The presentation should not be more than a maximum of 10 minutes duration.
 - A PowerPoint presentation is a must.
 - The maximum number of slides including the title slide should not be more than 8.
 - The presentation should contain the background of topic, literature survey, research gap, problem identification and research methodology.
- Provisionally eligible candidates will be allowed to appear only if they submit appropriate documents at the time of document verification (with a self-attested copy).


Dr. Z. M. Patel , Dr. J. N. Patel
Ph. D In-charges


Dr. (Ms.) Shilpi Gupta
Head, DoECE